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\*\* CONTINUING DATA \*\*\*\*\* *no 96*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *No, 96*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	CA	15	27	3
Examiner's Signature <i>[Signature]</i> Initials					

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## TITLE

Decision directed phase locked loops (DD-PLL) with excess processing power in digital communication systems

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